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COMPUTER AIDED DESIGN: LARGE-SIGNAL ANALYSIS

**Chairman: Stephen E. Sussman-Fort—State Univ. of NY
at Stony Brook**

Session Abstract: The ability to model the large signal behavior of GaAs FETs and other devices, as well as the large-signal behavior of circuits comprising these components continue to be of importance. This session includes papers which describe advances in the harmonic balance method—in particular using extremely efficient adjoint network techniques for gradient calculation—as well as applications of harmonic balance in obtaining large signal MESFET characteristics. Other papers discuss different approaches to large signal GaAs FET modeling, optimal design of power amplifiers, and load-pull characteristics of MESFETs.

**2:00 pm–3:30 pm, May 27, 1988
Jacob Javits Convention Center, Hall 1E
Room 4**